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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/049,812	12/27/2001	Eric J. Sprunk		7975

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EXAMINER
HOFFMAN, BRANDON S

ART UNIT	PAPER NUMBER
2136	

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/12/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary	Application No. 10/049,812	Applicant(s) SPRUNK ET AL.	
	Examiner Brandon S. Hoffman	Art Unit 2136	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 July 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 and 10-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 and 10-19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. Claims 1-7 and 10-19 are pending in this office action.
2. Applicant's arguments, filed July 27, 2006 (relating to the pre-appeal request), have been fully considered and they are persuasive.

Claim Rejections

3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 16-19 rejected under 35 U.S.C. 102(a/e) as being anticipated by Van Oorschot et al. (U.S. Patent No. 5,850,443).

Regarding claim 16, Van Oorschot et al. teaches a method of updating a cryptographic key used for decrypting distributed data, the method comprising:

- Generating a first key for decrypting the distributed data, the first key of a first length (col. 6, lines 25-29);
- Encrypting the first key with a second key, the second key of a second length, wherein the second length is longer than the first length (col. 6, lines 29-31); and
- Distributing the encrypted first key (fig. 1 and col. 6, lines 31-33).

Regarding claim 17, Van Oorschot et al. teaches further comprising distributing data encrypted with the first key (fig. 2).

Regarding claim 18, Van Oorschot et al. teaches further comprising:

- Generating a third key to replace the first key, the third key of a third length, wherein the third length is shorter than the second length (col. 6, lines 46-49);
- Encrypting the third key with the second key (col. 6, lines 43-46); and
- Distributing the encrypted third key (fig. 3).

Regarding claim 19, Van Oorschot et al. teaches further comprising distributing data encrypted with the third key (fig. 4).

6. Claims 1-7 and 10-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Menezes et al. (Handbook of Applied Cryptography, 1997, section 13.3.1, pages 551-553) in view of Weiant, Jr. et al. (U.S. Patent No. 6,044,350).

Regarding claim 1, Menezes et al. teaches an asymmetric cryptographic processing system using a multiple key hierarchy, the asymmetric cryptographic processing system comprising:

- A first key for performing asymmetric operations at a first rate, wherein each operation requires a first cryptographic processing time (page 552, step 3, *data keys*, provide cryptographic operations on user data, tend to be short-term keys); and
- A second key for performing an asymmetric cryptographic processing operation to update the first key (page 552, step 2, *key-encrypting keys*), wherein the second key is used for cryptographic processing operations for the first key at a second rate that is less often than the first rate (page 552, step 2, *key-encrypting keys*, the key-encrypting keys are used less often than the keys that they encrypt).

Menezes et al. does not specifically teach the second key requires a second cryptographic processing time greater than the first cryptographic processing time.

Weiant, Jr. et al. teaches the second key requires a second cryptographic processing time greater than the first cryptographic processing time (fig. 3).

It would have been obvious to one of ordinary skill in the art, at the time the invention was made, to combine the second key requiring more processing time than the first key, as taught by Weiant, Jr. et al., with the method/medium of Menezes et al. It would have been obvious for such modifications because longer length keys take more time to process, in order to provide more security, with a tradeoff that the key does not have to be replaced as often.

Regarding claims 2-5, Menezes et al. as modified by Weiant, Jr. et al. teaches wherein the system is used to cryptographically process and transfer digital [voice/audio/video] data in a network (see col. 3, lines 32-38 of Weiant, Jr. et al.).

Regarding claim 6, Menezes et al. as modified by Weiant, Jr. et al. teaches wherein the second key is hard coded into the system at the time of manufacturing the system (see page 551, section 13.3.1, step 1 of Menezes et al.).

Regarding claim 7, Menezes et al. as modified by Weiant, Jr. et al. teaches wherein a plurality of digital cryptographic processing systems are coupled by a telecommunications system, wherein the second key is distributed to two or more of the

asymmetric cryptographic processing systems via the telecommunications system (see fig. 2 of Weiant, Jr. et al.).

Regarding claim 10, Menezes et al. as modified by Weiant, Jr. et al. teaches a method for providing secure data transactions in a telecommunications system, wherein a digital processing device receives information from the telecommunications system (see fig. 2, ref. num 234 of Weiant, Jr. et al.), wherein the digital processing device uses a first asymmetrical cryptographically processed key to perform an asymmetric cryptographic processing operation to decode the information wherein the cryptographic processing operation is at a first level of complexity requiring a first amount of resources by the processing device (see page 552, step 3, *data keys* of Menezes et al.), wherein the cryptographic processing operation is performed at a first rate of cryptographic processing operations per unit time (see page 552, step 3, *data keys* of Menezes et al., provide cryptographic operations on user data, tend to be short-term keys), the method comprising:

- Transferring a second asymmetrical cryptographically processed key to the digital processing device, wherein the second asymmetrical cryptographically processed key is used in an asymmetric cryptographic processing operation at a second level of complexity requiring a second amount of resources by the processing device that is higher than the first amount of resources (see page 552, step 3, *data keys* of Menezes et al., the data keys are used, perhaps for every type of data they encrypt);

- Updating the first asymmetrical cryptographically processed key from time-to-time (see page 552, step 3 of Menezes et al.), wherein the updating of the first asymmetrical cryptographically processed key occurs at a second rate of cryptographic processing operations per unit time that is less than the first rate of cryptographic processing operations per unit time (see fig. 3, key B of Weiant, Jr. et al.), wherein the updating includes the following substeps:
 - Encoding a substitute first asymmetrical cryptographically processed key with a second key, so that the resulting cryptographically processed substitute first asymmetrical cryptographically processed key is decodable by the second asymmetrical cryptographically processed key (see page 552, paragraph below step 3 of Menezes et al., keys at one layer are used to protect items at a lower level); and
 - Transferring the substitute first asymmetrical cryptographically processed key to the digital processing device so that the substitute first asymmetrical cryptographically processed key is used in subsequent cryptographic processing operations by the digital processing device (see fig. 2, ref. num 234 of Weiant, Jr. et al.).

Regarding claim 11, Menezes et al. as modified by Weiant, Jr. et al. teaches further comprising:

- Transferring a third asymmetrical cryptographically processed key to the digital processing device (see page 551, section 13.3.1, step 1, master key of Menezes

et al.), wherein the third asymmetrical cryptographically processed key is used in an asymmetric cryptographic processing operation at a third level of complexity requiring a third amount of resources by the processing device that is higher than the second amount of resources (see page 551, section 13.3.1, step 1 of Menezes et al. and fig. 3, key C of Weiant, Jr. et al.);

- Updating the second asymmetrical cryptographically processed key from time-to-time (see page 552, step 2 of Menezes et al.), wherein the updating of the second asymmetrical cryptographically processed key occurs at a third rate of cryptographic processing operations per unit time that is less than the second rate of cryptographic processing operations per unit time (see fig. 3, key C of Weiant, Jr. et al.), wherein the updating includes the following substeps:
 - Encoding a substitute second asymmetrical cryptographically processed key with a third asymmetrical cryptographically processed key, so that the resulting cryptographically processed substitute second asymmetrical cryptographically processed key is capable of being cryptographically processed by the third asymmetrical cryptographically processed key (see page 552, paragraph below step 3 of Menezes et al., keys at one layer are used to protect items at a lower level); and
 - Transferring the substitute second asymmetrical cryptographically processed key to the digital processing device so that the substitute second asymmetrical cryptographically processed key is used in

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subsequent cryptographic processing operations by the digital processing device (see fig. 2, ref. num 234 of Weiant, Jr. et al.).

Regarding claims 12-15, the examiner takes Official Notice that the resources include [processing time/transistor density on an IC/memory capacity/data bandwidth] because these resources are well-known tradeoffs of resource intensive actions as cryptography.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brandon S. Hoffman whose telephone number is 571-272-3863. The examiner can normally be reached on M-F 8:30 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nasser G. Moazzami can be reached on 571-272-4195. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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[Signature]
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